

H.A

Notice of Allowability

Application No.

10/807,729

Applicant(s)

PETERS, FRANK H.

Examiner

Hung T. Vy

Art Unit

2821

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to the preliminary amendment filed on 8/24/2005.
2. ☒ The allowed claim(s) is/are 1-44.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date 6/30/2004
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☒ Interview Summary (PTO-413), Paper No./Mail Date _____.
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____.


WILSON LEE
PRIMARY EXAMINER

Reasons for Allowance

1. Claims 1-44 are allowed.

The following is an examiner's statement of reason for allowance:

None of the references of record teaches or suggests the claimed a photonic integrated circuit (PIC) comprising: a semiconductor laser and an electro-optic semiconductor modulator integrated on a substrate where said laser and modulator are aligned in and serially along an optical waveguide path, also formed on said substrate, said optical waveguide path substantially parallel with a planar extent of said substrate; activating the electro-optic components with the first and second applied **bias currents** whereby an undesired parasitic current is established along a first electrical path between them, the parasitic current also transferring modulated variations created at the second electro-optical component over to the first electro-optic component via the first electrical path so that the later no longer operates in its steady state condition; **an electrical isolation region** between the first and second electro-optic components to create a second electrical path transverse to the first electrical path; applying **a bias at the electrical isolation region** to divert the parasitic current from the electrical first path through the second electrical path to the bias point resulting in divided parasitic currents, respectively, between **the first electro-optic component** and the bias point and between the bias point and **the second electro-optic component** so that the undesired modulated variations along the first electrical path are substantially eliminated from affecting the first electro-optic component.


Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance".

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hung T. Vy whose telephone number is 571-2721954. The examiner can normally be reached on 8.30am - 5.30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Don Wong can be reached on 571 272 1834. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Hung T. Vy
Art Unit 2821
September 5, 2005.


WILSON LEE
PRIMARY EXAMINER